

Automotive TV Tuner Device Family



CXD2885GG-W

DVB-T, DVB-T2

ISDB-T, ATSC3.0

4 diver Dual Channel AEC-Q100 Grade 2

TS^{Serial} Parallel USB2.0 SDIO3.0



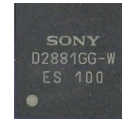
CXD2886GG-W

DVB-T, DVB-T2

ISDB-T

4 diver Dual Channel AEC-Q100 Grade 2

TS^{Serial} Parallel USB2.0 SDIO3.0



CXD2881GG-W

DVB-T, DVB-T2

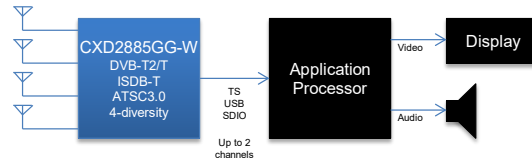
ISDB-T

2 diver Dual Channel

TS^{Serial} SPI SDIO2.0

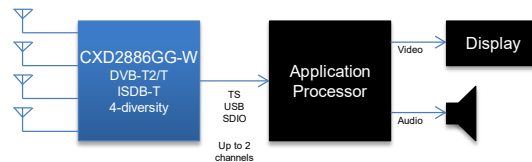
CXD2885GG-W

CXD2885GG-W is a high performance 4-diversity MRC multi-standard tuner LSI suitable for world wide applications. The latest ATSC3.0 demodulation algorithms are incorporated in an optimized single package, low bill of materials solution. AEC-Q100 is applied to the reliability tests.



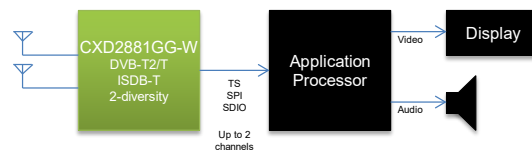
CXD2886GG-W

CXD2886GG-W is a high performance 4-diversity MRC multi-standard tuner LSI suitable for world wide applications. DVB-T2, DVB-T and ISDB-T demodulation algorithms are incorporated in an optimized single package, low bill of materials solution. AEC-Q100 is applied to the reliability tests.



CXD2881GG-W

CXD2881GG-W is a 2-diversity MRC tuner LSI suitable for DVB and ISDB countries. The low noise tuner combined with optimized demodulation algorithms ensures clear reception in tough mobile signal environments.



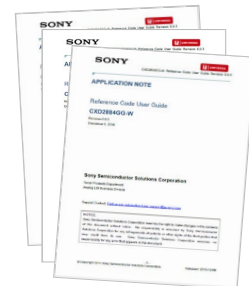
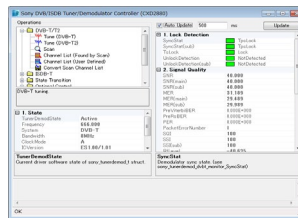
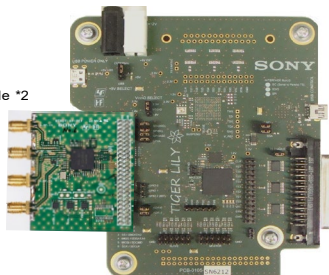
	CXD2885GG-W	CXD2886GG-W	CXD2881GG-W
Target markets	Europe, ASEAN, India, Middle East, Africa, Japan, Latin America, North America, South Korea	Europe, ASEAN, India, Middle East, Africa, Japan, Latin America	Europe, ASEAN, India, Middle East, Africa, Japan, Latin America
Broadcasting standard	DVB-T2/T2 lite DVB-T ISDB-T/Tsb/Tmm ATSC3.0	DVB-T2/T2 lite DVB-T ISDB-T/Tsb/Tmm	DVB-T2/T2 lite DVB-T ISDB-T/Tsb/Tmm
Reception band	UHF: 470MHz to 862MHz VHF: 54MHz to 230MHz	UHF: 470MHz to 862MHz VHF: 54MHz to 230MHz	UHF: 470MHz to 862MHz VHF: 50MHz to 470MHz
Multi-channel streaming	Up to 2 channels	Up to 2 channels	Up to 2 channels
SoC interface *1	Control: I2C, USB, SDIO Streaming: TS _{x2} , USB, SDIO	Control: I2C, USB, SDIO Streaming: TS _{x2} , USB, SDIO	Control: I2C, SPI _{x2} , SDIO _{x2} Streaming: TS _{x2} , SPI _{x2} , SDIO _{x2}
Supply voltage	Core: 1.1V I/F: 1.8V or 3.3V	Core: 1.1V I/F: 1.8V or 3.3V	Core: 1.1V, 1.8V I/F: 1.8V or 3.3V
Operating temperature	T _J -40C to 125C	T _J -40C to 125C	T _A -40C to 85C
Package	21mm x 21mm LFBGA	21mm x 21mm LFBGA	10mm x 10mm LFBGA
Bill of material benefits	No external memory	No external memory	No external memory
Reliability test level	AEC-Q100 Grade 2	AEC-Q100 Grade 2	JEDEC + Japanese Tier1 requirements
Schedule	ES: Ready MP: Please contact us	ES: Ready MP: Please contact us	ES: Ready MP: Please contact us

*1 TS serial is supported on all devices. TS parallel output is supported on CXD2885GG-W and CXD2886GG-W.

DEVELOPMENT KIT

- Design collateral
- Datasheet
 - Application notes
 - H/W schematics, BOM
 - S/W driver
 - Linux application example *2

- Evaluation collateral
- Evaluation board
 - Windows GUI
 - Reference test results



*2 NXP i.MX6 or Raspberry Pi reference design kit with TV application software is available for CXD2885GG-W, CXD2886GG-W and CXD2881GG-W.

REGIONAL SUPPORT

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